IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Jeffrey S. Mailloux et al.	Examiner: Hong Kim
Serial No.:	08/650,719	Group Art Unit: 2751
Filed:	May 20, 1996	Docket: 303.623US1
Title:	ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION	

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

Box AF
Assistant Commissioner for Patents
Washington, D.C. 20231

In response to the final Office Action mailed June 30, 1999, please amend the application as follows.

IN THE CLAIMS

Please amend the claims as follows:

50.   (Amended) A system comprising:

a microprocessor;

a memory, coupled to the microprocessor, the memory [in] selectively operable either in a burst mode or a pipelined mode, wherein the memory is an asynchronous dynamic random access memory; and

a system clock coupled to the microprocessor.

REMARKS

Claim 50 is amended. Claims 1-9, 33-35, 46, and 48-50 are now pending in this application. Applicant respectfully requests reconsideration of the above-identified patent application as amended and in view of the following remarks.